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L20	request near3 l6	2072	L20
L19	l7 and L18 and l11	0	L19
L18	l1 near3 L17	2841	L18
L17	schedul$	181080	L17
L16	l7 and l4 and l11	4	L16
L15	l14 and l13	3	L15
L14	('20050027936' |'6735679' |'6167492')!.ABPN1,NRPN,PN,TBAN,WKU.	6	L14
L13	l3 and l12	42	L13
L12	l4 and L11	222	L12
L11	l9 or l10	3981	L11
L10	711/167-169.ccls.	2221	L10
L9	711/154-155.ccls.	2007	L9
L8	l4 and l7	91	L8
L7	l3 with l6	433	L7
L6	fifo	44490	L6

L5	l3 and l4	2138	L5
L4	l1 near3 l2	19315	L4
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L12	l4 and L11	222	L12
L11	l9 or l10	3981	L11
L10	711/167-169.ccls.	2221	L10
L9	711/154-155.ccls.	2007	L9
L8	l4 and l7	91	L8
L7	l3 with l6	433	L7
L6	fifo	44490	L6
L5	l3 and l4	2138	L5
L4	l1 near3 l2	19315	L4
L3	process$ near2 order	126406	L3
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<u>L17</u>	schedul$	181080	<u>L17</u>
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<u>L11</u>	l9 or l10	3981	<u>L11</u>
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<u>L8</u>	l4 and l7	91	<u>L8</u>
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Application#	Patent#	Status	Date Filed	Title	Inventor Name 39
60552576	Not Issued	159	03/11/2004	VARIOUS METHODS AND APPARATUSES FOR WIDTH AND BURST CONVERSION	WEBER, WOLF-DIETRICH
60084795	Not Issued	159	05/08/1998	CACHE COHERENCE UNIT FOR INTERCONNECTING MULTIPROCESSOR NODES HAVING A SPARSE DIRECTORY WITH ENHANCED REPLACEMENT CONTROL	WEBER, WOLF-DIETRICH
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11077743	Not Issued	020	03/10/2005	VARIOUS METHODS AND APPARATUSES FOR WIDTH AND BURST CONVERSION	WEBER, WOLF-DIETRICH
10963271	Not Issued	030	10/11/2004	METHOD AND APPARATUS FOR SCHEDULING A RESOURCE TO MEET QUALITY-OF-SERVICE RESTRICTIONS	WEBER, WOLF-DIETRICH
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10917682	Not Issued	020	08/13/2004	RESOLVING CROSSING REQUESTS IN MULTI-NODE CONFIGURATIONS	WEBER, WOLF-DIETRICH
10698905	Not Issued	030	10/31/2003	METHOD AND APPARATUS FOR ESTABLISHING A QUALITY OF SERVICE MODEL	WEBER, WOLF-DIETRICH
10678380	Not Issued	020	10/03/2003	LOW POWER SHARED LINK ARBITRATION	WEBER, WOLF-DIETRICH
10418624	Not Issued	041	04/18/2003	METHOD AND APPARATUS FOR ROUND ROBIN RESOURCE ARBITRATION	WEBER, WOLF-DIETRICH
10418370	Not Issued	030	04/18/2003	VARIOUS METHODS AND APPARATUSES FOR ARBITRATION AMONG BLOCKS	WEBER, WOLF-DIETRICH

				OF FUNCTIONALITY	
10409806	6804757	150	04/08/2003	METHOD AND APPARATUS FOR SCHEDULING REQUESTS USING ORDERED STAGES OF SCHEDULING CRITERIA	WEBER, WOLF-DIETRICH
10330591	Not Issued	030	12/27/2002	METHOD AND APPARATUS FOR AUTOMATIC CONFIGURATION OF MULTIPLE ON-CHIP INTERCONNECTS	WEBER, WOLF-DIETRICH
10288973	Not Issued	061	11/05/2002	METHOD AND APPARATUS FOR CONFIGURABLE ADDRESS MAPPING AND PROTECTION ARCHITECTURE AND HARDWARE FOR ON-CHIP SYSTEMS	WEBER, WOLF-DIETRICH
10285987	Not Issued	030	11/01/2002	METHOD AND APPARATUS FOR ERROR HANDLING IN NETWORKS	WEBER, WOLF-DIETRICH
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10138946	Not Issued	030	05/03/2002	COMPOSING ON-CHIP INTERCONNECTS WITH CONFIGURABLE INTERFACES	WEBER, WOLF-DIETRICH
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09977510	Not Issued	071	10/12/2001	METHOD AND APPARATUS FOR SCHEDULING OF REQUESTS TO DYNAMIC RANDOM ACCESS MEMORY DEVICE	WEBER, WOLF-DIETRICH
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06252841	4357278	250	04/10/1981	PROCESS FOR SYNTHESIZING ESTRONE OR ESTRONE DERIVATIVES	WEBER, WOLF-DIETRICH

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